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first, second, and third editions of his book Phaselock Techniques [1] [2] have been highly influential and remains a well-recognized reference among electrical engineers specializing in areas involving PLL 's. Moreover, he has written a remarkable IEEE paper dedicated to the description, the modelling and the characterization of

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the widely used Charge-Pump Phase-Lock Loops (see references below).

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In the 1st edition of his well-known work, Phaselock Techniques, Floyd M. Gardner introduced a lock-in concept: If, for some reason, the frequency difference between input and VCO is less than the loop bandwidth, the loop will lock up almost instantaneously without slipping cycles.

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